

## WEST Search History





DATE: Saturday, March 03, 2007

| <u>Hide?</u>             | <u>Set Name</u>                                | <u>Query</u>                                 | <u>Hit Count</u> |
|--------------------------|--|--|------------------|
|                          | <i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>           |  |                  |
| <input type="checkbox"/> | L43  | l1 and L42                                   | 3                |
| <input type="checkbox"/> | L42  | 712/214.ccls.                                | 344              |
| <input type="checkbox"/> | L41  | l1 and L40                                   | 2                |
| <input type="checkbox"/> | L40  | 712/219.ccls.                                | 333              |
| <input type="checkbox"/> | L39  | weighted fair queueing                       | 131              |
| <input type="checkbox"/> | L38  | tcp routing same thread\$1                   | 1                |
| <input type="checkbox"/> | L37  | packet switching same thread\$1              | 11               |
| <input type="checkbox"/> | L36  | 733153.ap.                                   | 3                |
| <input type="checkbox"/> | L35  | cisco.as. and steiss.in.                     | 1                |
|                          | <i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i> |  |                  |
| <input type="checkbox"/> | L34  | cisco.as. and steiss.in.                     | 1                |
|                          | <i>DB=PGPB; PLUR=NO; OP=ADJ</i>                |  |                  |
| <input type="checkbox"/> | L33  | US-20040215933-A1.did.                       | 1                |
| <input type="checkbox"/> | L32  | US-20040215933-A1.did.                       | 1                |
|                          | <i>DB=EPAB; PLUR=NO; OP=ADJ</i>                |  |                  |
| <input type="checkbox"/> | L31  | WO-2005013129-A1.did.                        | 1                |
| <input type="checkbox"/> | L30  | WO-2005013129-A1.did.                        | 1                |
|                          | <i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i> |  |                  |
| <input type="checkbox"/> | L29  | instruction\$1 and selector\$1 and thread\$1 | 19               |
| <input type="checkbox"/> | L28  | instruction\$1 and L27                       | 26               |
| <input type="checkbox"/> | L27  | (mux or multiplex\$3) and thread\$1          | 120              |
|                          | <i>DB=USPT; PLUR=NO; OP=ADJ</i>                |  |                  |
| <input type="checkbox"/> | L26  | US-6931516-B2.did.                           | 1                |
| <input type="checkbox"/> | L25  | US-6931516-B2.did.                           | 1                |
|                          | <i>DB=PGPB; PLUR=NO; OP=ADJ</i>                |  |                  |
| <input type="checkbox"/> | L24  | US-20050240752-A1.did.                       | 1                |
| <input type="checkbox"/> | L23  | US-20050240752-A1.did.                       | 1                |
| <input type="checkbox"/> | L22  | US-20060179280-A1.did.                       | 1                |
|                          | <i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i> |  |                  |
| <input type="checkbox"/> | L21  | thread\$1 and L20                            | 5                |
| <input type="checkbox"/> | L20  | decoded instruction\$1                       | 819              |

*DB=PGPB,USPT; PLUR=NO; OP=ADJ*

|                          |     |  |      |
|--------------------------|-----|--|------|
| <input type="checkbox"/> | L19 | thread\$3 same L18   | 1    |
| <input type="checkbox"/> | L18 | upper pipeline\$1 same lower pipeline\$1                                     | 40   |
| <input type="checkbox"/> | L17 | L16 not L6   | 18   |
| <input type="checkbox"/> | L16 | (exception\$1 or trap\$4 or interrupt\$4 or fault\$3 or stall\$3) with L5    | 187  |
| <input type="checkbox"/> | L15 | (mux or multiplex\$3 or select\$3) same L14                                  | 1    |
| <input type="checkbox"/> | L14 | (exception\$1 or trap\$4 or interrupt\$4 or fault\$3) same blocked thread\$1 | 46   |
| <input type="checkbox"/> | L13 | L11 with select\$3   | 43   |
| <input type="checkbox"/> | L12 | (mux or multiplex\$3) same L11   | 11   |
| <input type="checkbox"/> | L11 | (exception\$1 or trap\$4 or interrupt\$4 or fault\$3) with L10               | 576  |
| <input type="checkbox"/> | L10 | dispatch\$3 with instruction\$1  | 5751 |
| <input type="checkbox"/> | L9  | (mux or multiplex\$3) same L6  | 1    |
| <input type="checkbox"/> | L8  | L6 with priorit\$8   | 38   |
| <input type="checkbox"/> | L7  | (decod\$3 with instruction\$1) same L6                                       | 1    |
| <input type="checkbox"/> | L6  | (exception\$1 or trap\$4 or interrupt\$4 or fault\$3) with L5                | 169  |
| <input type="checkbox"/> | L5  | dispatch\$3 with thread\$1   | 1486 |
| <input type="checkbox"/> | L4  | L3 not L2  | 0    |
| <input type="checkbox"/> | L3  | treat.xp. and (thread\$3 same packet\$1)                                     | 3    |
| <input type="checkbox"/> | L2  | treat.xp. and thread\$3.clm. and packet\$1                                   | 3    |
| <input type="checkbox"/> | L1  | thread\$1 same round robin same priorit\$7                                   | 262  |

END OF SEARCH HISTORY